



PTO/SB/08A (05-03)

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<b>Substitute for form 1449A/PTO</b>				<b>Complete if Known</b>	
				Application Number	10/627,218
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				Filing Date	July 25, 2003
				First Named Inventor	Yeo, <i>et al.</i>
				Art Unit	Unknown
				Examiner Name	Unknown
(use as many sheets as necessary)				Attorney Docket Number	TSM03-0556
Sheet	1	of	1		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
JH	A	US-6,258,664 B1	07-10-2001	Reinberg	
	B	US-6,358,791 B1	03-19-2002	Hsu, <i>et al.</i>	
	C	US-2002/0153549 A1	10-24-2002	Laibowitz, <i>et al.</i>	
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Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)				
JH	H	WO 03/017336 A2	02-27-2003	Amberwave Systems Corporation		

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
JH	I	WANG, L.K., <i>et al.</i> , "On-Chip Decoupling Capacitor Design to Reduce Switching-Noise-Induced Instability in CMOS/SOI VLSI," Proceedings of the 1995 IEEE International SOI Conference, Oct. 1995, pp. 100-101.	
	J	YEOH, J.C., <i>et al.</i> , "MOS Gated Si:SiGe Quantum Wells Formed by Anodic Oxidation," Semicond. Sci. Technol. (1998), Vol. 13, pp. 1442-1445, IOP Publishing Ltd., UK.	
	K	CAVASSILAS, N., <i>et al.</i> , "Capacitance-Voltage Characteristics of Metal-Oxide-Strained Semiconductor Si/SiGe Heterostructures," Nanotech 2002, Vol. 1, pp. 600-603.	

Examiner Signature		Date Considered	5/5/05
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